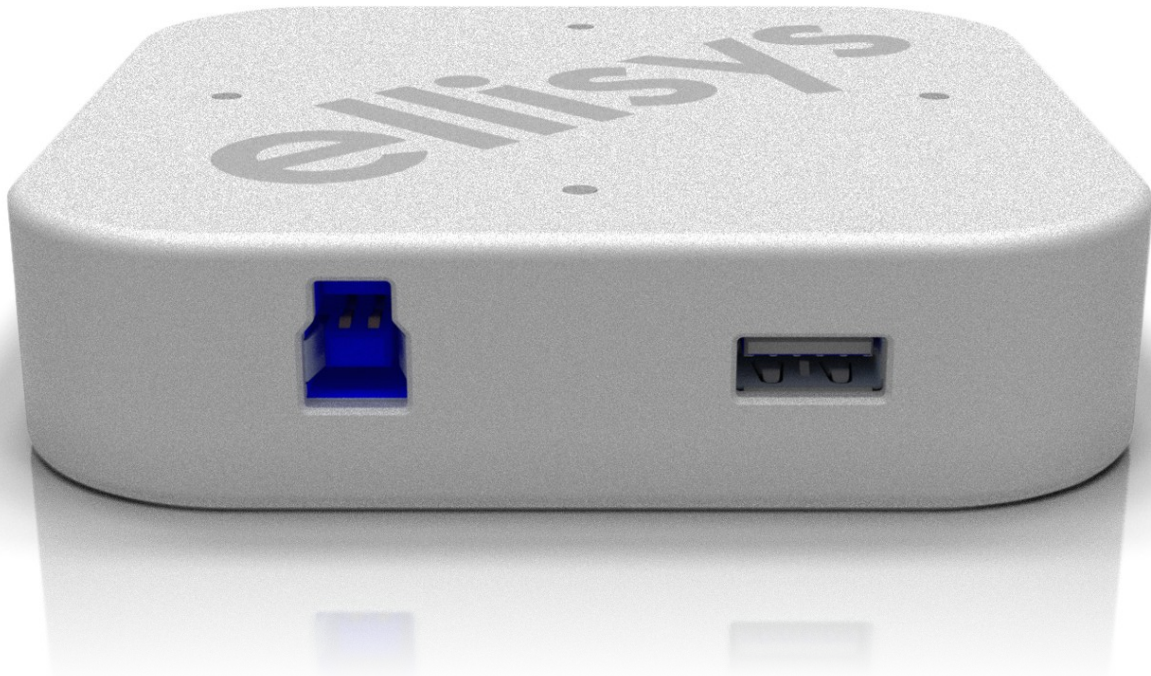


Multi-function USB 3.1 & PD Protocol Test Platform

Ellisys USB Explorer 350



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Key Points:

- **Specs Support:** USB 3.1, USB 2.0, USB Power Delivery 1.3 & 2.0 and Type-C 1.0
- **Speeds Support:** USB 3.1 at 5 / 10 Gbps and USB 2.0 at 1.5 / 12 / 480 Mbps
- **Multi-function:** Same box supports protocol analysis, host / device emulation, and compliance
- **Flexible Front-end:** Adaptive equalization support, configurable emphasis and swing
- **Integrated Logic Analyzer:** Concurrent capture and display of FPGA / ASIC outputs
- **Perfect Precision:** Designed to have the highest fidelity down to the bit
- **Intuitive Software:** Easy-to-use and smartly configurable user interface
- **Full Development Life-cycle:** From early prototyping to certification
- **Responsive Support:** From engineers to engineers
- **Free Maintenance:** Free lifetime updates, free fully featured viewer



Ellisys USB Explorer 350

USB 3.1 & PD Protocol Test Platform



Revolutionary protocol analyzer and traffic generator supporting USB 3.1, USB 3.0, USB 2.0, and USB Power Delivery 2.0

Overview

The revolutionary USB Explorer 350 (EX350) is a super-compact yet sophisticated and comprehensive protocol test and analysis system for USB 3.1 (Gen1 and Gen2), USB 2.0, and USB Power Delivery 2.0.

With many innovative and exclusive features, the USB Explorer 350 meets the demands of today's technology developers by providing unmatched coverage for protocol analysis, traffic generation, and compliance testing for emerging USB technologies.

The hardware is economically optimized to perform analysis, active emulation of port traffic, and automated compliance testing.

Protocol Analysis

Protocol analysis is accomplished non-intrusively, using next-generation hardware capture technology, including the use of adaptive equalization and precision impedance matching. The hazards of signal retiming are eliminated.

The powerful EX350 concurrently captures USB 2.0 traffic, USB 3.1 traffic (Gen1 and Gen2), USB Power Delivery 2.0 traffic (FSK and Baseband), as well as Vbus characteristics and logic signals. The integrated logic analysis provides developers with a new dimension enabling them to efficiently visualize external digital signals such as outputs of their FPGA / ASIC in addition to their USB traffic.

Captured traffic is presented real-time by sixth-generation Ellisys application software, intuitively detailing concurrent traffic, protocol sequences, performance criteria, error conditions, electrical characteristics, timing details, USB standard / class decoding, and more.

Traffic Generation

Emulation of USB host, device, and hub traffic as well as USB Power Delivery sink and source traffic is accomplished using dedicated applications that simplify these complex tasks. Sophisticated software stacks drive the emulation using ultra-fast processors specially designed for these tasks.

A wide array of features are provided for error injection scenarios in order to test software and hardware corner cases and margins. Performance characterization is provided by specialized hardware that is optimized to stress receiver buffer management, credit logic, and application software.

Compliance Testing [Option]

Ellisys participates closely in USB-IF compliance groups and is dedicated to provide best-of-breed compliance testing solution for USB 3.1 and USB Power Delivery 2.0.

This sold-separately compliance software embeds tens of test cases covering various aspects of the physical layer, link layer and protocol layer. The same setup is used at compliance workshops, making it the perfect tool for pre-compliance testing.

Test results are pass/fail, annotated with descriptive detail, and are summarized in a convenient HTML-formatted summary report upon completion of testing. Protocol traces from each test can optionally be captured automatically with the protocol analyzer, providing for quick and in-depth analysis of test results.

Examiner is an essential tool for IC, IP, or device manufacturers preparing for certification. Examiner is not only useful at the end of the development cycle, but from the beginning to the end for ensuring non-regressions with new improvements.



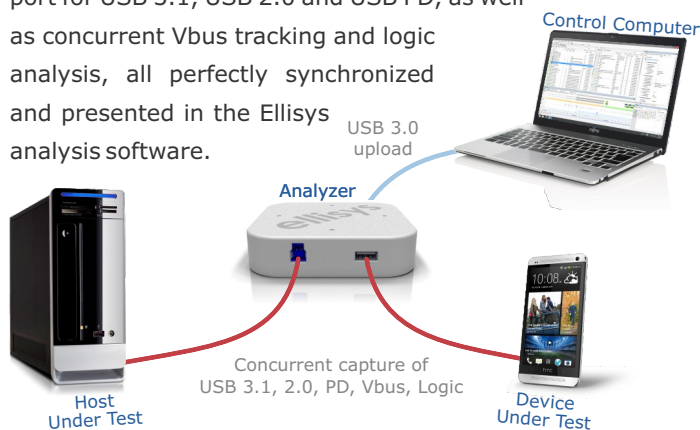
Unique Ellisys Features

- **Broad specifications support:** USB 2.0 (1.5, 12, 480 Mbps), USB 3.1 (5, 10 Gbps) and USB Power Delivery 2.0 (FSK, Baseband)
- **Multi-function:** Same unit can operate as protocol analyzer, host / device / sink / source emulator, as well as compliance tester, enabling flexible allocation of units depending project evolution
- **Compact:** The unit can be placed optimally even on the most busy environment, which is particularly critical with tight margins of USB 3.1 at 10 Gbps
- **Perfect Accuracy:** High precision clocking components coupled to Ellisys' brand new protocol analysis engine provides industry's unmatched accuracy
- **Unequaled Software:** Ellisys powerful yet easy to use multi-protocol analysis software offers the most complete feature set of the industry, including essential features such as Instant Timing for precise timing analysis and Instant Throughput for performance characterization
- **Integrated Logic Analysis:** Logic signals analysis concurrently to traffic capture opens a new debugging dimension to development engineers by visualizing outputs of their FPGA or ASIC
- **Free Maintenance:** free lifetime updates as well as free fully-featured viewer software with unlocked hardware that can be used on any computer, supported from engineers to engineers

Typical Setups

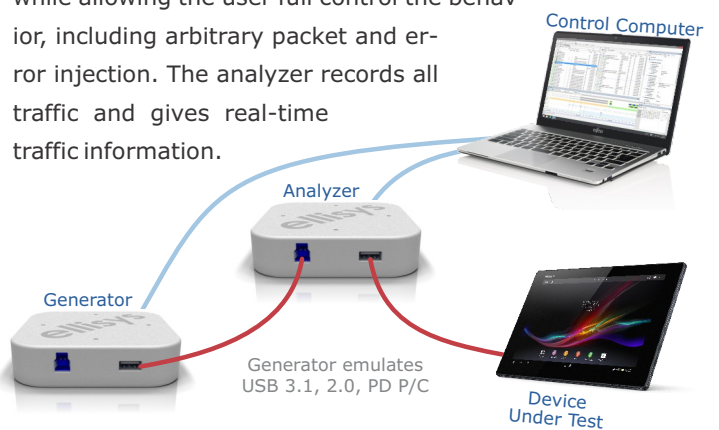
Analysis Setup

The USB Explorer 350 is connected in a logically passive mode to record traffic and bus events passing between host and device. The analyzer provides same-link concurrent support for USB 3.1, USB 2.0 and USB PD, as well as concurrent Vbus tracking and logic analysis, all perfectly synchronized and presented in the Ellisys analysis software.



Host Emulation Setup

An USB Explorer 350 acts as generator while the second unit acts as passive protocol analyzer. The generator emulates a USB host with PD dual-role Provider/Consumer support, while allowing the user full control the behavior, including arbitrary packet and error injection. The analyzer records all traffic and gives real-time traffic information.





Ellisys USB Explorer 350

USB 3.1 & PD Protocol Test Platform



Technical Specifications

Connectors

- **Link under test:** Std USB 3.1/PD A/B
- **Logic:** 2 x 8 inputs / outputs with configurable level and threshold
- **Sync:** Ability to synchronize units by sharing the same clock
- **Control:** Optimized USB 3.0 connection
- **Power:** 9-24VDC, min 12W

Supported Protocols

- Power Delivery 2.0 (FSK and Baseband)
- USB 1.0 / USB 1.1 (1.5 and 12 Mbps)
- USB 2.0 (1.5, 12 Mbps and 480 Mbps)
- USB 3.0 (5 Gbps)
- USB 3.1 (10 Gbps)
- The availability of the protocols depends on the edition

Supported modes

- Analyzer: non-intrusive protocol analyzer
- Generator: traffic generator for emulating host, device, sink, source
- Examiner: compliance tester
- The availability of the modes depends on the options

Analyzer Capabilities

- Concurrent capture of USB 2.0, USB 3.x, Power Delivery, Vbus and logic signals
- Multiple-unit synchronization capability
- Automatic termination detection
- Pre-capture traffic filtering

Generator Capabilities

- Emulate USB host or device
- Emulate PD sink or source
- Precise and reproducible timings
- Ability to emulate low-level issues as well as high-level behaviors

USB 1.x / 2.0 Capabilities

- Support of Low, Full and High-speed
- Automatic speed detection
- Precise tracking of line states
- Non-intrusive probing
- Timing resolution of 16.7 ns

USB 3.x Capabilities

- Support of 5 Gbps and 10 Gbps
- Automatic speed detection
- Automatic termination detection
- Non-intrusive signal reshaping
- Flexible front-end with adaptive equalization and configurable emphasis and swing
- Timing resolution of 400 ps

USB Power Delivery Capabilities

- Non-intrusive probing
- Support of FSK and Baseband PHY
- Support of Type-C CC via custom cable
- Measurement of FSK carrier frequency, amplitude and bit rate
- Configurable FSK amplitude in generator mode

Vbus Capabilities

- Measurement of Vbus from 0 to 25V
- Measurement of Ibus from -5 to +5A
- Generation of Vbus from 4 to 20V

Logic Capabilities

- 16 inputs (max 5V) / outputs (1.2 to 3.3V)
- Configurable level and threshold (2 banks of 8 signals)
- Timing resolution of 400 ns
- Bandwidth of 50 MHz

Power Adapter

- Universal 100-240 VAC, 50-60 Hz
- 24 VDC, 18 W

Enclosure

- 106 x 106 x 20 mm (4.2 x 4.2 x 0.8")
- 0.3 kg (0.6 lbs)

Maintenance and Licensing

- Free lifetime updates – no maintenance fees
- Free full-featured viewer application – easily share annotated traces between computers and colleagues
- Use Ellisys hardware on any computer – no additional licenses needed

Product Warranty

- Two years limited warranty

Ordering Information

Description	Code
Ellisys USB Explorer 350 Analyzer for PD (Protocol analyzer supporting USB 2.0 and Power Delivery)	EX350-PD-A
Ellisys USB Explorer 350 DUO for PD (Two analyzer / generator units supporting USB 2.0 and Power Delivery)	EX350-PD-DUO
Ellisys USB Explorer 350 Analyzer for SSP (Protocol analyzer supporting USB 2.0, USB 3.0, USB 3.1 and Power Delivery)	EX350-SSP-A
Ellisys USB Explorer 350 DUO for SSP (Two analyzer / generator units supporting USB 2.0, USB 3.0, USB 3.1 and Power Delivery)	EX350-SSP-DUO

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Configurations

Edition	PD	SSP
Power Delivery	x	x
USB 2.0 (1.5, 12, 480 Mbps)	x	x
USB 3.0 (5 Gbps)		x
USB 3.1 (10 Gbps)		x

Configuration	A	DUO
Provided units	1	2
Protocol analyzer	x	x
Traffic generator		x

